

PALM INTRANET

Day : Tuesday
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Inventor Name Search Result

Your Search was:

Last Name = SRIKANTAM

First Name = VAMSI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09655206	6275083	150	09/05/2000	Low operational power, low leakage power D-type flip-flop	SRIKANTAM, VAMSI
09828321	6601230	150	04/05/2001	LOW POWER CIRCUIT DESIGN THROUGH JUDICIOUS MODULE SELECTION	SRIKANTAM, VAMSI K.
10443503	7096374	150	05/21/2003	METHOD AND APPARATUS FOR DEFINING AN INPUT STATE VECTOR THAT ACHIEVES LOW POWER CONSUMPTION IN DIGITAL CIRCUIT IN AN IDLE STATE	SRIKANTAM, VAMSI K.
10443555	7085942	150	05/21/2003	METHOD AND APPARATUS FOR DEFINING AN INPUT STATE VECTOR THAT ACHIEVES LOW POWER CONSUMPTION IN A DIGITAL CIRCUIT IN AN IDLE STATE	SRIKANTAM, VAMSI K.
10461531	6822481	150	06/12/2003	METHOD AND APPARATUS FOR CLOCK GATING CLOCK TREES TO REDUCE POWER DISSIPATION	SRIKANTAM, VAMSI K.
10814472	Not Issued	71	03/29/2004	Digital modulator employing a polyphase up-converter structure	SRIKANTAM, VAMSI K.
11120698	7148828	150	05/03/2005	SYSTEM AND METHOD FOR TIMING CALIBRATION OF TIME-INTERLEAVED DATA CONVERTERS	SRIKANTAM, VAMSI K.
11192827	Not Issued	30	07/28/2005	Imaging serial interface ROM	SRIKANTAM, VAMSI K.
11192894	Not Issued	30	07/28/2005	Imaging parallel interface RAM	SRIKANTAM, VAMSI K.
11292472	7339853	150	12/02/2005	TIME STAMPING EVENTS FOR FRACTIONS OF A CLOCK CYCLE	SRIKANTAM, VAMSI KRISHNA
11292477	Not Issued	95	12/02/2005	TRIGGERING EVENTS AT FRACTIONS OF A CLOCK CYCLE	SRIKANTAM, VAMSI KRISHNA
11408400	Not Issued	30	04/21/2006	Programmable trigger delays	SRIKANTAM, VAMSI KRISHNA

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